

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	544500	(scan adj chain) same (register) same (scan adj mode) samd (clock)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/06 16:56
L2	1	(SOC) same ((single adj step) adj execution)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/06 17:15
S1	2214	716/4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 14:43
S2	1710	716/5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 14:08
S3	1764	716/1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 14:08
S4	2214	(716/4)." adj (debug\$ same (SOC or (system adj on adj chip)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 14:52
S5	12	(716/4).ccls. and (debug\$ same (SOC or (system adj on adj chip)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 14:57
S6	6	(716/5).ccls. and (debug\$ same (SOC or (system adj on adj chip)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 14:57
S7	36	("716"/\$).ccls. and (debug\$ same (SOC or (system adj on adj chip)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 14:58
S8	17	("716"/1).ccls. and (debug\$ same (SOC or (system adj on adj chip)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 14:59

S9	218	(debug\$ same (SOC or (system adj on adj chip)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 15:00
S10	65	(debug\$ same (SOC or (system adj on adj chip))) and (clock and (breakpoint or (break adj point)) and trigg\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 16:08
S11	0	(debug\$ same (SOC or (system adj on adj chip))) and (clock same (breakpoint or (break adj point)) same trigg\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 15:02
S12	2	("6754852").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 15:12
S13	9	(debug\$ same (SOC or (system adj on adj chip))) and (clock adj control) and ((breakpoint or (break adj point)) and trigg\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 15:53
S14	0	(debug\$ same (SOC or (system adj on adj chip))) and (clock adj control) and ((breakpoint or (break adj point)) same event) and trigg\$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 15:59
S15	0	(debug\$ same (SOC or (system adj on adj chip))) and (clock adj control) and ((breakpoint or (break adj point)) same event)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 15:59
S16	4	(debug\$ same (SOC or (system adj on adj chip))) and (clock and (breakpoint or (break adj point)) and trigg\$) and (cycle same stepping)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 16:10
S17	99	(debug\$ and (SOC or (system adj on adj chip))) and (clock and (breakpoint or (break adj point)) and trigg\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 10:02
S18	71	(debug\$ same (SOC or (system adj on adj chip))) and (clock and (breakpoint or (break adj point)) and trigg\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 10:02

S19	11	(debug\$ same (SOC or (system adj on adj chip))) and (clock adj control) and ((breakpoint or (break adj point)) same event\$2) and (trigg\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 10:38
S20	1	((debug\$ same (SOC or (system adj on adj chip))) and (clock adj control) and ((breakpoint or (break adj point)) same event\$2) and (trigg\$)).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 10:39
S21	1	(debug\$ same (SOC or (system adj on adj chip))) and (functional adj block\$2) and (clock adj control) and (debug adj trigger) and ((breakpoint or (break adj point)) same event\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 10:44
S22	1	((debug\$ same (SOC or (system adj on adj chip))) and (functional adj block\$2) and (clock adj control) and (debug adj trigger) and ((breakpoint or (break adj point)) same event\$2)).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 10:47
S23	1	((debug\$ same (SOC or (system adj on adj chip))) and ((functional adj block\$2) same (clock adj control)) and (debug adj trigger) and event\$2).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 10:51
S24	1	((debug\$ same (SOC or (system adj on adj chip))) and ((functional adj block\$2) and (clock adj control)) and (debug adj trigger) and event\$2).CLM.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 10:51
S25	2	((debug\$ same (SOC or (system adj on adj chip))) and ((functional adj block\$2) and (clock adj control)) and (debug adj trigger) and event\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/05 10:51
S26	3	(debug\$ same (SOC or (system adj on adj chip)) and (function\$3 adj block\$2) same (clock adj control))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/06 13:06
S27	5	(debug\$ same (SOC or (system adj on adj chip)) and (function\$3 adj block\$2) and (clock adj control))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/06 13:06
S28	19	(breakpoint\$4 same event\$2) and (monitor\$4 adj events)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/06 17:12

S29	1	(breakpoint\$4 same event\$2) and (monitor\$4 adj events) and (SOC)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/06 15:55
S30	11	(debug\$ same (SOC or (system adj on adj chip))) and (clock adj control) and (breakpoint same event\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/06 16:21
S32	4	(debug\$ same (SOC or (system adj on adj chip))) same (functional adj blocks)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/06 16:22
S33	52	(debug\$ same (SOC or (system adj on adj chip))) and (functional adj blocks)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/06 16:34



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... 4-2 4.2 **Clock** interfaces ... 8-16 Table 8-14 Read PC value after **debug** state entry ... 20-10 Table 20-5 Test port connections in **functional** mode ...

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